

| Type | L # | Hits | Search Text | DBS | Time Stamp | Comments |
|------|-----|------|--|------------------------|---------------------|----------|
| 1 | BRS | L1 0 | memory and queue and data and (link or path) and packet and dma and (backup or redundant or failsafe) and (processor or computer) and (fault same tolerant) and checkpointing and (data same replication) | US- PGPUB; USPAT | 2005/06/24 09:48 | |
| 2 | BRS | L2 9 | memory and queue and data and (link or path) and packet and dma and (backup or redundant or failsafe) and (processor or computer) and (fault same tolerant) and (data same replication) | US- PGPUB; USPAT | 2005/06/24 09:48 | |

| Type | Hits | Search Text | DBs |
|------|--------|---|------------------|
| 1 | BRS 0 | memory and (memory same segment) and queue and (link or path) and (fault same tolerant) and redundant and failsafe and backup and (data same coherency) | US- PGPUB; USPAT |
| 2 | BRS 29 | memory and (memory same segment) and queue and (link or path) and (fault same tolerant) and (data same coherency) | US- PGPUB; USPAT |